

Notice of References Cited

Application/Control No.

O9/717,966

Examiner

Chun Cao

Applicant(s)/Patent Under
Reexamination
EMONS, MARTIJN JOHANNES

Art Unit
Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-5,481,731	01-1996	Conary et al.	711/141
	В	US-5,530,932	06-1996	Carmean et al.	713/324
	С	US-5,669,003	09-1997	Carmean et al.	713/322
	D	US-5,790,875	08-1998	Andersin et al.	713/320
	Ε	US-5,845,139	12-1998	Fischer et al.	713/324
	F	US-6,105,141	08-2000	Hanlon et al.	713/323
	G	US-6,128,747	10-2000	Thoulon, Pierre-Yves	/ 713/330
	Н	US-6,266,776	07-2001	Sakai, Makoto	713/300
	ı	US-6,347,294	02-2002	Booker et al.	703/28
	J	US-6,523,128	02-2003	Stapleton et al.	713/330
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	Z					
	0					
	Р					
	Q					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	υ	Bajwa et al., "Instruction Buffering to Reduce Power in Processors for Signal Processing" IEEE Transactions on Very Large Scale Integration (VLSI) System, vol. 5, No. 4, December 1997, pp 417-424.
	٧	
	8	
	х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.